WEST Search History

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DATE: Saturday, October 30, 2004

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L44	bops.as. and shared and local	6
☐	L43	bops.as. and global and local	3
☐	L42	bops.as. and (global same local)	0
☐	L41	6366999.pn.	1
☐	L40	l35 not l38	38
☐	L39	l35 not l38L38	503
☐	L38	l35 not l37	465
☐	L37	L36 not sun.as.	38
☐	L36	vliw and L35	65
☐	L35	global register$1	503
☐	L34	global register$1 and L33	10
☐	L33	712/24.ccls.	228
		DB=USPT; PLUR=NO; OP=OR	
☐	L32	(synchroniz$5 and vliw and 5574939.uref.)	10
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L31	synchroniz$5 and L29	10
☐	L30	synchriniz$5 and L29	0
☐	L29	vliw and L28	29
☐	L28	5574939.uref.	54
		DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ	
☐	L27	5574939.uref.	0
☐	L26	clustered vliw	2
☐	L25	multiple vliw cores	0
☐	L24	multiple vliw cores	0
☐	L23	vliw and L22	12
☐	L22	multiprocessor or multi-processor or (multiple processors)	12450
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L21	L6 with vliw	50
☐	L20	clustered vliw	5
☐	L19	variable issue width	10
☐	L18	synchroniz$5 same L17	4

☐	L17	vliw processors	299
☐	L16	multiple vliw cores	2
☐	L15	multiple vliw processors	0
☐	L14	multiple vliw pipelines	0
☐	L13	vliw same L12	25
☐	L12	multiple pipelines	419
☐	L11	vliw and L10	5
☐	L10	5459798.uref.	22
☐	L9	vliw same L8	4
☐	L8	L6 with chip	1040
☐	L7	L6 adj chip	56
☐	L6	multiprocessor or multi-processor or (multiple processors)	26036
☐	L5	vliw and L4	6
☐	L4	pipeline paths	24
☐	L3	L1 same L2	12
☐	L2	"two register files"	172
☐	L1	"single register file"	148

END OF SEARCH HISTORY